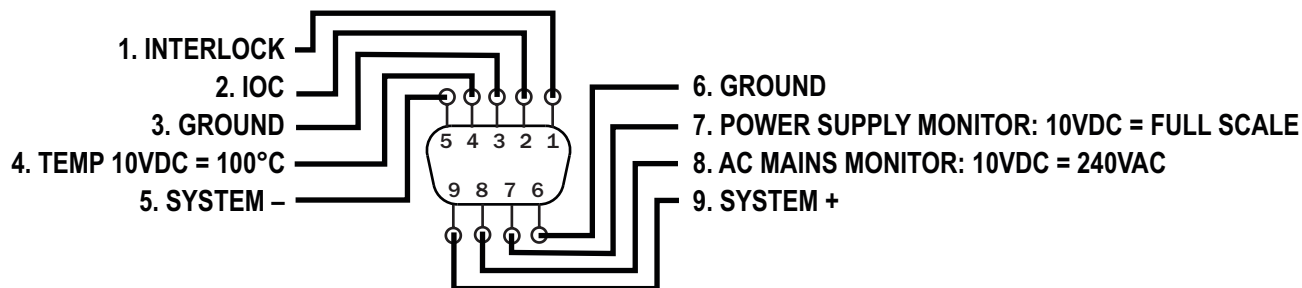


PINOUTS FOR INTERCONNECT PORTS (A & B)

8300, 8500, and 8700 Series



DIP SWITCH SETTINGS

8300, 8500, and 8700 Series



DEFAULT DIP SWITCH SETTINGS SHOWN

DIP SWITCH SETTINGS

	UP	DOWN	
1 UNBALANCED INPUT	ON	OFF	Enable unbalanced input signal connector
2 BALANCED INPUT	ON	OFF	Enable balanced input signal connector
3 DC SERVO	ON	OFF	Enable DC Servo
4 OPERATION MODE	CV	CC	Controlled-voltage or controlled-current operation
5 COMPENSATION NETWORK 2	ON	OFF	Enable Compensation network #2 (for controlled-current operation)
6 COMPENSATION NETWORK 1	ON	OFF	Enable Compensation network #1 (for controlled-current operation)
7 CONTROL CONFIGURATION	MASTER	FOLLOWER	Multi-amp configuration
8 COUPLING	DC	AC	DC enable or DC block
9 GAIN BIT 8 (MSB)	20	OFF	Set Amplifier Gain (0 to 40)
10 GAIN BIT 7	10	OFF	
11 GAIN BIT 6	5	OFF	
12 GAIN BIT 5	2.5	OFF	
13 GAIN BIT 4	1.25	OFF	
14 GAIN BIT 3	0.63	OFF	
15 GAIN BIT 2	0.31	OFF	
16 GAIN BIT 1 (LSB)	0.16	OFF	
17 ELECTRONIC GAIN MATCHING	ON	OFF	Enable electronic gain matching (for parallel multi-amp operation)
18 SYNTHETIC IMPEDANCE BIT 3 (MSB)	0.5Ω	OFF	Enable Synthetic Impedance (0.125 ohms to 0.875 ohms)
19 SYNTHETIC IMPEDANCE BIT 2	0.25Ω	OFF	
20 SYNTHETIC IMPEDANCE BIT 1 (LSB)	0.125Ω	OFF	
21 CURRENT LIMIT BIT 4 (MSB)	+80A	OFF	Enable Current Limit (10A to 150A)
22 CURRENT LIMIT BIT 3	+40A	OFF	
23 CURRENT LIMIT BIT 2	+20A	OFF	
24 CURRENT LIMIT BIT 1 (LSB)	+10A	OFF	

NOTE: ALL BIT SWITCHES ARE ADDITIVE. UP = ON.

RED = FACTORY DEFAULT